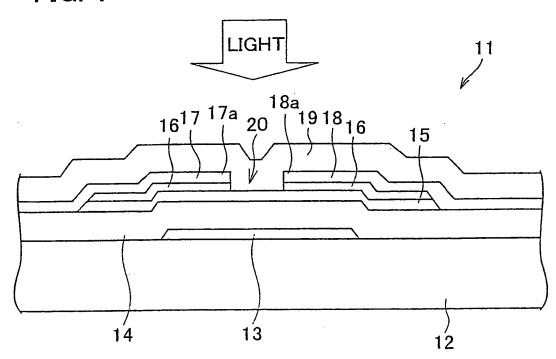
Wills Section

FIG. 1



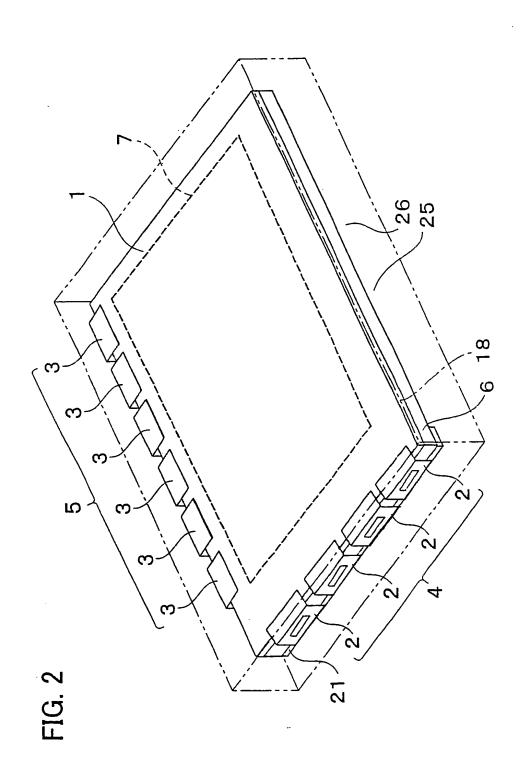


FIG. 3 5 READING CIRCUIT -D2 D3 -D1 Dm (**G**2 DRIVING CIRCUIT (G3 Gn ه Vss

FIG. 4

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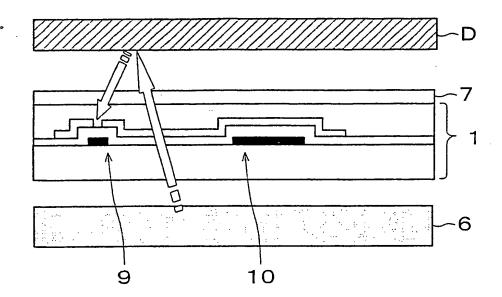


FIG. 5

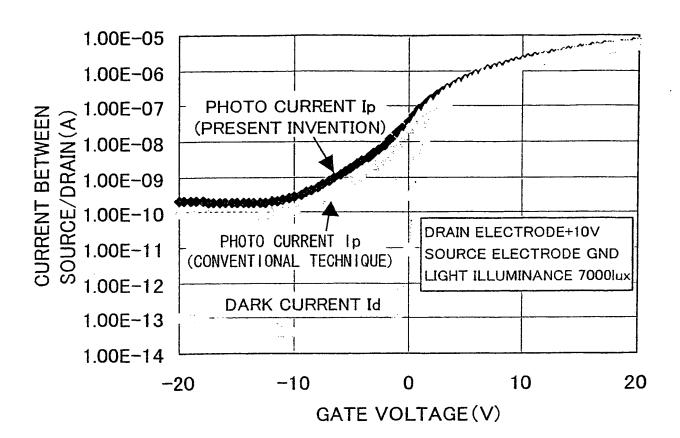


FIG. 6

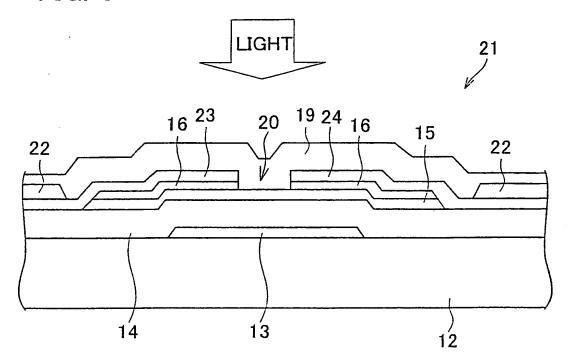


FIG. 7 (a)

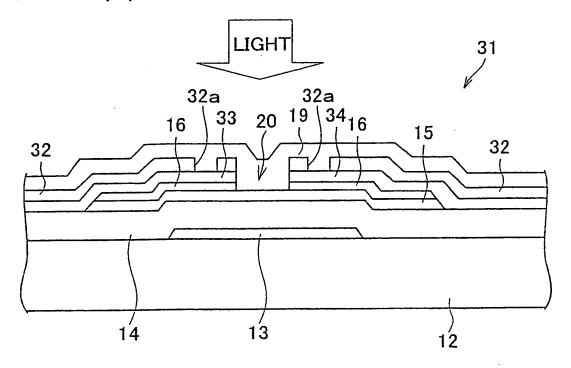


FIG. 8 (a)

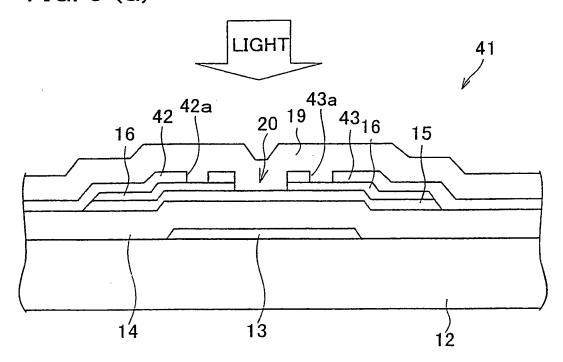


FIG. 8 (b)

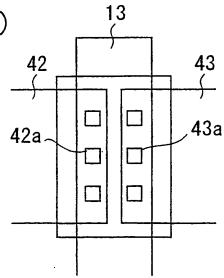
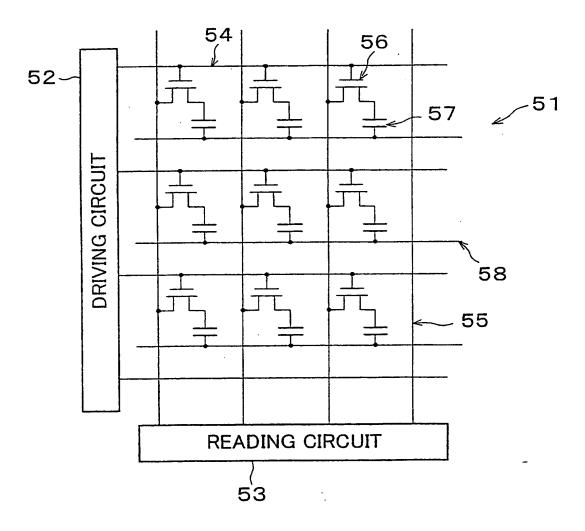


FIG. 9



. The State of C

FIG. 10

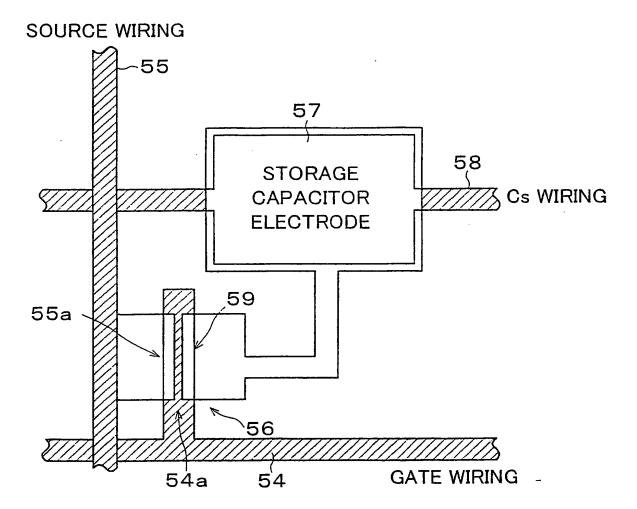


FIG. 11

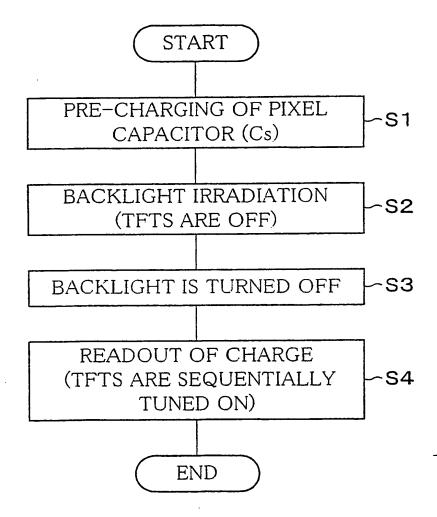


FIG. 12

